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Bib Data Sheet

CONFIRMATION NO. 1091

SERIAL NUMBER 10/773,276	FILING DATE 02/09/2004  RULE	CLASS 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. 2004_0156A
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 \*\* CONTINUING DATA ~~N~~ <sup>VS</sup> \*\*\*\*\*

 \*\* FOREIGN APPLICATIONS <sup>VS</sup> \*\*\*\*\*  
 JAPAN 2003-032438 02/10/2003 NO

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 06/14/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature <u>[Signature]</u> Initials <u>VS</u>	JAPAN	17	22	6

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## TITLE

Logic circuit optimizing method, logic circuit optimizing device and logic circuit composing device

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